

Tom van Dijk

Curriculum Vitae

Formal Models and Verification
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github: [trolando](#)

I'm interested in bridging the gap between low-level programming and developing complex and parallel algorithms. My approach to problem solving thus combines operational understanding (in terms of how low-level algorithms *think*) with abstract reasoning.

I am a postdoctoral researcher at the FMV group in Linz. My current research is on parity games, where I develop new efficient algorithms to solve parity games and work on a fast practical implementation of existing algorithms. Parity games are an important tool to solve verification and synthesis problems related to, for example, LTL properties and ω -automata, which are popular techniques in formal verification. Some other research interests are fast and efficient tools, lock-free algorithms, and formal verification.

I also study parallelizing SAT-solving with prof.dr. Armin Biere. I have implemented various lock-free algorithms, that is, algorithms that do not require explicit locking techniques to avoid problems. Lock-free datastructures and algorithms are challenging because different threads are working on the same bytes in memory. With modern computers, there are then many ways to introduce bugs. This is why implementations typically use locking. Lock-free programming is sometimes the only way to obtain scalable performance on multi-core computers.

Education

2012 – 2016 **Ph.D.**, *Formal Methods and Tools*, University of Twente, NL.

Thesis: [Sylvan: Multi-core Decision Diagrams](#).

2010 – 2012 **M.Sc.**, *University of Twente*, NL, *cum laude*.

Thesis: [The parallelization of binary decision diagram operations for model checking](#).

Won the honorary mention (2nd place) of the Dutch M&I Informatie Scriptieprijs 2012.

2003 – 2010 **B.Sc.**, *University of Twente*, NL.

Thesis: [Analysing And Improving Hash Table Performance](#).

Combined with Applied Physics in 2003–2005.

Employment History

Academic

2016 – 2018 **Postdoc**, *Formal Models and Verification*, Johannes Kepler University, Linz.

Topics: parity games and parallel satisfiability solving.

- Implemented Oink, a new tool & library for solving parity games, a fundamental method for the model checking of transition systems and for synthesis problems ([Oink on Github](#)).
- Invented a new approach to solve parity games based on an improved understanding of their underlying structure, which outperforms all previous algorithms.

2012 – 2016 **Ph.D.**, *Formal Methods and Tools*, University of Twente, Enschede.

Topic: low-level parallel/multi-core binary decision diagrams

- The first scalable multi-core implementation of binary decision diagrams, scaling up to 40× on a 64-core machine ([Sylvan on Github](#)).
- A low-level high-performance task parallelism framework ([Lace on Github](#)).
- Significantly faster (and parallel) implementation of symbolic bisimulation minimisation for LTS, CTMC, IMC ([SigrefMC on Github](#)).

Software Engineering

- 2007 – 2009 **Freelance Developer**, *Scangineers*, *Scanpoint*, Amersfoort, NL.
Writing real-time controller software for an R&D project involving self checkout counters.
- Responsible for all programming, in C, Java and Assembly.
 - Debugging kernel drivers and inter-process communication.
 - Real-time processing of large amounts of data from different sensors and cameras.
 - After the initial project, I was hired again to do a follow-up project.
 - Both projects were successfully completed.
- 2004 – 2010 **Web developer**, *Almexx*, Enschede, NL.
Freelance web development mostly in Java, creating webapps that calculate and compare the prices and interest rates of various financial products, such as mortgages and deposits.
- 2000 – 2003 **Development of game modding tools**.
During high school, I developed several game modification tools in Visual C++ to customize the computer game The Sims, in particular IFF Pencil 2. I developed these programs by reverse engineering the raw data files. One of these let the user change the jobs a sim could pursue. This program was published and sold worldwide under the name Career Creator Pro. Another tool, Script Station, involved reverse engineering the internal scripting language of The Sims from the data files.

Awards

- Awarded an honorary mention (2nd place) of the Dutch national M&I Informatie Scriptieprijs 2012 for my Master thesis ([pdf](#)).

Talks

- 2018 **Invited talk**, *Parallel Logical Reasoning*, Oxford, UK.
Invited talk on using work-stealing to parallelize various different algorithms in formal verification.

Teaching

Courses

- 2018 **Guest lecture**, *Institute for Advanced Computer Science*, Leiden, NL.
Taught a guest lecture about parity games, the current state of the art, with emphasis on the attractor-based algorithms, to students of the model checking course.
- 2017 – 2018 **Parallel programming course at the JKU**.
- 2× a lecture on Luce, load balancing, lock-free programming.
 - Designed appropriate homework assignments.
- 2013 – 2015 **Languages & Machines (Basismodellen in de Informatica)**.
- Course in the Bachelor of Computer Science, in 2013, 2014 and 2015.
 - Taught the tutorial sessions.
- 2013 – 2014 **Modeling and Analysis of Concurrent Systems 2**.
- Course in the Master of Computer Science, in 2013 and 2014.
 - Designed the lectures on BDDs and on IC3 in model checking.
 - Assisted with the practical assignment.

Student supervision

- 2015 **Supervised Master student Wytse Oortwijn**.
Topic: Symbolic state space exploration with distributed BDDs
- 2015 **Supervised Bachelor student Thijs van Ede**.
Topic: Verifying lockless dequeues
- 2014 **Co-supervised Bachelor student Simon de Vries**.
Topic: Optimizing state vector compression by reordering program variables

- 2013 **Supervised Master student Maryam Haji Ghasemi.**
Topic: Zero-suppressed binary decision diagrams in model checking

Academic Service

- 2018 **FMCAD webmaster.**
For the 2018 edition of FMCAD, I am keeping the website updated.
- 2017 **Competition co-organizer.**
With Armin Biere and Keijo Heljanko, I co-organized the Hardware Model Checking Competition 2017.
- 2016 **Workshop co-organizer.**
I co-organized the Dutch Model Checking Day 2016, which this year had a focus on symbolic approaches to model checking, as well as parallel computation of model checking algorithms.
- 2014 – **External conference reviewer.**
SPIN 2014, ICFEM 2014, FASE 2016, ATVA 2016, TACAS 2017, FCT 2017, TACAS 2018, SAT 2018, FORTE 2018, CSL 2018.
- 2014 – **Journal reviewer.**
IEEE Transactions on Computers (2014), Formal Methods in System Design (2018), Software Tools for Technology Transfer (2018).

Research Visits and Summer Schools

- 2018 **DIMAP/FoCS**, short research visit to Marcin Jurdziński and colleagues on parity games.
- 2016 **SAT/SMT/AR**, summer school on SAT, SMT solving and Automated Reasoning.
- 2015 **ISCASMC**, short research visit to the group of Lijun Zhang in Beijing.
- 2014 **MOVES**, short research visit to the group of Joost-Pieter Katoen in Aachen.
- 2014 **SAT/SMT**, summer school on SAT and SMT solving.
- 2013 **Marktobendorf**, two-week summer school on Software Systems Safety.
- 2012 **VTSA**, summer school on Verification Technology, Systems & Applications.

Other activities

- student life Secretary of the board (2006–2007) of a student association with about 90 active members and several weekly activities.
- voluntary work Chairman (2012–2013) of a regional department of a national political youth organisation.
- voluntary work As a member of two study groups Economy and Sustainability of a political youth organisation, I co-organized a national symposium (“Duurzaam denken & duurzaam ondernemen”, January 2015) on environmental sustainability for entrepreneurs.
- voluntary work As part of a group of volunteers (2012–2018), I developed several open source web applications in Python and Django to support the internal organisation of a political youth organisation.

Publications

Google Scholar (May 2018): Number of citations: 165. H-index: 6

Journals

- [doi](#) 2018 Dijk, Tom van and Jaco van de Pol. “Multi-core symbolic bisimulation minimisation”. In: *STTT* 20.2 (2018), pp. 157–177.
- [doi pdf](#) 2017 Dijk, Tom van and Jaco van de Pol. “Sylvan: multi-core framework for decision diagrams”. In: *STTT* 19.6 (2017), pp. 675–696.

Conferences and workshops

- pdf 2018 Dijk, Tom van. "Attracting Tangles to Solve Parity Games". In: *CAV*. 2018.
- doi pdf 2018 Dijk, Tom van. "Oink: An Implementation and Evaluation of Modern Parity Game Solvers". In: *TACAS (1)*. Vol. 10805. Lecture Notes in Computer Science. Springer, 2018, pp. 291–308.
- doi 2017 Biere, Armin, Tom van Dijk, and Keijo Heljanko. "Hardware model checking competition 2017". In: *FMCAD*. IEEE, 2017, p. 9.
- doi 2017 Dijk, Tom van, Robert Wille, and Robert Meolic. "Tagged BDDs: Combining reduction rules from different decision diagram types". In: *FMCAD*. IEEE, 2017, pp. 108–115.
- doi 2017 Oortwijn, Wytse, Tom van Dijk, and Jaco van de Pol. "Distributed binary decision diagrams for symbolic reachability". In: *SPIN*. **Best paper award**. ACM, 2017, pp. 21–30.
- doi pdf 2016 Dijk, Tom van and Jaco van de Pol. "Multi-core Symbolic Bisimulation Minimisation". In: *TACAS*. Vol. 9636. Lecture Notes in Computer Science. Springer, 2016, pp. 332–348.
- doi 2015 Dijk, Tom van, Ernst Moritz Hahn, David N. Jansen, Yong Li, Thomas Neele, Mariëlle Stoelinga, Andrea Turrini, and Lijun Zhang. "A Comparative Study of BDD Packages for Probabilistic Symbolic Model Checking". In: *SETTA*. Vol. 9409. LNCS. Springer, 2015, pp. 35–51.
- doi pdf 2015 Dijk, Tom van and Jaco van de Pol. "Sylvan: Multi-Core Decision Diagrams". In: *TACAS 2015*. 2015, pp. 677–691.
- doi 2015 Kant, Gijs, Alfons Laarman, Jeroen Meijer, Jaco van de Pol, Stefan Blom, and Tom van Dijk. "LTSmin: High-Performance Language-Independent Model Checking". In: *TACAS 2015*. 2015, pp. 692–707.
- doi 2015 Oortwijn, Wytse, Tom van Dijk, and Jaco van de Pol. "A Distributed Hash Table for Shared Memory". In: *Parallel Processing and Applied Mathematics*. Springer, 2015.
- doi 2014 Dijk, Tom van and Jaco van de Pol. "Lace: Non-blocking Split Deque for Work-Stealing". In: *MuCoCoS*. Vol. 8806. LNCS. Springer, 2014, pp. 206–217.
- doi 2012 Dijk, Tom van, Alfons W. Laarman, and Jaco van de Pol. "Multi-core and/or Symbolic Model Checking". In: *AVOCS*. Vol. 53. 2012.
- doi 2012 Dijk, Tom van, Alfons Laarman, and Jaco van de Pol. "Multi-Core BDD Operations for Symbolic Reachability". In: *PDMC*. Vol. 296. ENTCS. 2012, pp. 127–143.

Other

- pdf 2018 Dijk, Tom van, Rüdiger Ehlers, and Armin Biere. "Revisiting Decision Diagrams for SAT". arXiv. 2018.
- doi 2018 Dijk, Tom van and Jaco van de Pol. "Multi-core Decision Diagrams". In: *Handbook of Parallel Constraint Reasoning*. Springer, 2018, pp. 509–545.
- doi pdf 2016 Dijk, Tom van. "Sylvan: multi-core decision diagrams". PhD thesis. University of Twente, Enschede, Netherlands, 2016.